



**EXPEDITED PROCEDURE – EXAMINING GROUP 1722**

**S/N 10/692,611**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Mohan Chandra et al.	Examiner:	G. Nagesh Rao
Serial No.:	10/692,611	Group Art Unit:	1722
Filed:	October 24, 2003	Docket No.:	884.B40US1
Title:	DEVICE AND METHOD FOR MULTICRYSTALLINE SILICON WAFERS		
Assignee:	Intel Corporation	Customer Number:	21186

**AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116**

Mail Stop AF  
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In response to the Final Office Action mailed September 6, 2006, Applicant proposes an amendment to the application as follows:

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